IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/749,624 Confirmation No. : 9861
Applicant : Eric JACOBSEN, et al
Filed : December 20, 2003
TC/A.U. : TBA
Examiner : TBA
Docket No. : P16732
Customer No. : 25694
Title : Adaptive Bit Loading with Low Density Parity Check
Forward Error Correction

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §§ 1.97 and 1.98

Mail Stop: Amendment
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P.O. Box 1450
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Sir:

In accordance with the duty of disclosure under 37 CFR §1.56, Applicants hereby notify the U.S. Patent and Trademark Office of the documents which are listed on the attached Form PTO-1449 and/or listed herein and which the Examiner may deem relevant to patentability of the claims of the above-identified application.

The present Information Disclosure Statement is being filed (1) no later than three months from the application's filing date or (2) before the mailing date of the first Office Action on the merits (whichever is later), and therefore no certification under 37 C.F.R. §1.97(e) or fee under 37 C.F.R. §1.17(p) is required.

Docket No.: 42P16732
Serial No.: 10/749,624

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

Dated: June 16, 2004



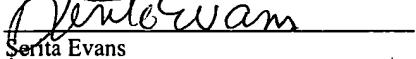
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CERTIFICATE OF MAILING

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By:



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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
Sheet	1		1	Application Number	10/749,624
				Filing Date	Dec. 20, 2003
				First Named Inventor	Eric Jacobsen
				Art Unit	TBA
				Examiner Name	TBA
				Attorney Docket Number	42P16732

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ²
		Boyd Bangerter, et. al., "High-Throughput Wireless LAN Air Interface," Intel Technology Journal, Volume 7, Issue 3, Published August 19, 2003, ISSN 1535-864X, pp. 47-57 (USA).		
		Eric Jacobsen, "LDPC FEC for IEEE 802.11n Applications" (Power Point Presentation), presented to IEEE 802.11 plenary session, Task Group n, on November 10, 2003, slides 1-35.		

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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